

**Anna University Approved
SEVEN DAYS
FACULTY DEVELOPMENT TRAINING
PROGRAMME**

on

EE 8251 – CIRCUIT THEORY

27.11.2017 to 03.12.2017

REGISTRATION FORM

Name :

Sex : Male/Female

Qualification :

Department :

Designation :

Organization :

**Address for
Communication** :

Phone No. :

E.mail ID :

Signature of the Candidate

Signature of Head of the Department
with seal

ORGANIZING COMMITTEE

CHIEF PATRON :

Vice-Chancellor,
Anna University, Chennai

Prof. M. Mala
CEO, JCE, Chennai

PATRON:

Dr. S. Ganesan
Registrar, Anna University, Chennai

Dr. M. Ramalingam,
Principal, JCE, Chennai

CHAIR:

Dr. K. Shanthi
Director, CFD

Dr. D. Sridharan
Addl. Director, CFD

CO-ORDINATORS:

Dr. S.Nagarajan
Professor & Head – EEE, JCE

Dr. N. Booma
Professor – EEE, JCE

Venue: Seminar Hall
Department of EEE, JCE

IMPORTANT DATES:

Submission of Application : 23.11.2017
Intimation of Selection : 24.11.2017
Confirmation by Participants : 25.11.2017

SEVEN DAYS FDTP

ON

EE 8251 – CIRCUIT THEORY

27th November - 3rd December, 2017

Co-ordinators
Dr. S.NAGARAJAN
Dr. N. BOOMA



Approved by

Anna University
Chennai

Organized by



Department of Electrical and Electronics
Engineering

Jerusalem College of Engineering

'A NAAC Accredited Institute'

Pallikaranai, Chennai – 600 100

ABOUT THE COLLEGE

Jerusalem College of Engineering, affiliated to Anna University and recognized by AICTE was founded in 1995 and is placed in the heart of the city. As a self financing institution, this college was promoted by Jerusalem Educational Trust.

Prof. M. Mala, the CEO, an Educationist and a person of determination has been the pillar behind the continuous up-gradation of standards in imparting technical education in this college. Academicians and excellent faculty members nevertheless play the pivotal role in grooming the students to excel in academic and other activities.

ABOUT THE DEPARTMENT

Department of Electrical and Electronics Engineering was established in the year 1995. The post graduate programme M.E-Power Electronics and Drives was established in the year of 2003. The department is recognized as a collaborative research centre with Anna University in the year of 2004. The department was accredited by NBA & NAAC. Several Research Projects have been funded by AICTE through MODROB and RPS Schemes. The department, in all areas, is being supported by several experienced and skilled faculty members and staff.

The department is well equipped with laboratories and has computing facilities with latest software like MATLAB, PSPICE, ETAP and PSCAD etc.

The areas of expertise of the faculty members include power electronics, power systems, Machines and Drives, Robotics, induction heating, renewable energy etc.

OBJECTIVES OF FDTP

Teaching-learning is an important process in academic institutions. Transfer of knowledge takes place from the teacher to the students only when the teaching is effective.

The young teachers need orientation and the training in effective teaching to provide service to the student community.

The objective of this programme is to provide with a comprehensive understanding of Circuit Theory. The course presents an illustrative approach to Electrical Circuits.

ELIGIBILITY & SELECTION

Teaching faculty from Affiliated Engineering Colleges of Anna University are eligible to attend the programme. Number of participants is limited to 25. Priority will be given for young faculty members. The participants will be selected based on First Come First Serve basis.

REGISTRATION DETAILS & INTIMATION

All the duly filled and signed registration forms should be sent to the coordinator by post or courier. A scanned copy of the filled application may be emailed to the coordinator. Intimation of selection will be informed through E-mail.

No Registration Fee

E-Mail Id : fdtpcircuit17@gmail.com

PROGRAMME CERTIFICATES

Participation certificates will be issued to candidates who attend the full programme, by the Centre for Faculty Development, Anna University, Chennai – 25

COURSE CONTENT

Comprehensive and Simulation based Analysis of following topics.

- DC and AC circuits.
- Theorems for network reduction.
- Transient response analysis.
- Three phase circuits.
- Concept of Resonance

RESOURCE PERSONS

Eminent Speakers from various reputed Educational Institutes and Industries.

CO-COORDINATORS

S. Rudhra , AP - EEE,JCE

S. Lavanya Devi, AP - EEE,JCE

ADDRESS FOR COMMUNICATION

The Coordinator;
Department of Electrical and Electronics Engg,
Jerusalem College of Engineering,
Velachery Main Road, Narayanapuram,
Pallikaranai, Chennai – 600 100.

Ph. : 9884324943

Email : fdtpcircuit17@gmail.com

Website : www.jerusalemengg.ac.in